REMARKS

The present application is a continuation of U.S. Patent Application Serial No. 09/828,488 for: "Method and Circuit for Increasing the Memory Access Speed of an Enhanced Synchronous SDRAM" filed April 5, 2001. In the parent application, the claims 1–27 were finally rejected under finally rejected under 35 U.S.C. § 103(a) over *Dosaka et al.* (U.S. Patent No. 5,680,363) and *Dosaka '92* (Dosaka, K. et al, "A 100-MHz 4-Mb Cache DRAM with Fast Copy-Back Scheme," in view of *Cho* (U.S. Patent No. 5,025,421) or *Sartore et al.* (U.S. Patent No. 5,721,862); *Kessler et al.* (U.S. Patent No. 6,546,453) or *Shimomura et al.* (U.S. Patent No. 6,526,471) or *Quach.* (U.S. Patent No. 5,950,220); and *Tomita* (U.S. Patent No. 5,881,009).

In the present application, claims 1, 12 and 23 are amended to specify that the row cache is a "single row cache." Support for the amendment can be found in Fig. 2 and the accompanying description in the specification from page 10, line 14 to page 11, line 9.

A <u>single</u> row cache is neither described nor suggested by U.S. Patent No. 5,680,363 to *Dosaka et al*, and *Dosaka '92* (*i.e.*, Dosaka, K. et al, "A 100-MHz-4-Mb Cache DRAM with Fast Copy-Back Scheme," in view of U.S. Patent No. 5,025,421 to *Cho* (or U.S. Patent No. 5,721,862 to *Sartore*), and U.S. Patent No. 6,546,453 to *Kessler et al* (or U.S. Patent No. 6,526,471 to *Shimomura*, or U.S. Patent No. 5,950,220 to *Quach*), and, finally, U.S. Patent No. 5,881,009 to *Tomita*. Instead, these references all describe conventional caches. Accordingly, claims 1, 12, and 23, as amended, are allowable over the references. Claims 2–11, 13–22, and 24–27, which depend from claims 1, 12, and 23, respectively, are allowable for at least the same reason.

Accordingly, claims 1–27 are believed to be allowable and the case in condition for allowance, which action is respectfully requested. If the Examiner has any questions, she or he is requested to contact the undersigned at the telephone number listed below.

Respectfully submitted,

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